

1/12/03

Sheet 1 of 1

Form PTO-1449  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use several sheets if necessary)	ATTY. DOCKET NO.  YOR920030184US1 (8728-625)	SERIAL NO. 10/661,299 Not Yet Assigned
	APPLICANT Howard Hao Chen et al.	
	FILING DATE Concurrently Herewith	GROUP ART UNIT 2823

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
BU		5,154,661	10/13/92	Higgins	62	3.3	7/10/91
BU		5,259,737	11/9/93	Kamisuki et al.	417	322	7/2/91
BU		5,313,094	5/17/94	Beyer et al.	257	622	1/8/92
BU		5,569,950	10/29/96	Lewis et al.	257	467	11/28/95
BU		5,753,529	5/19/98	Chang et al.	437	67	5/19/95
BU		5,757,081	5/26/98	Chang et al.	257	788	2/20/96
BU		5,759,014	6/2/98	Van Lintel	417	413.3	1/12/95
BU		5,767,578	6/16/98	Chang et al.q	257	717	4/19/96
BU		6,080,608	6/27/00	Nowak	438	151	8/6/98

FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)			
BU		Larry Gilg et al., "Die Products: ideal IC packaging for demanding applications", Electronic Design, December 23, 2002, pp. 47-50.	
BU		M. Amacost et al., "Plasma-etching processes for ULSI semiconductor circuits", IBM J. Res. Develop., Vol. 43, No. 1/2, January/March 1999, pp. 39-72.	

EXAMINER Brook Kibede	DATE CONSIDERED 5/11/2005
* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

(Form PTO-1449 (6-4))

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